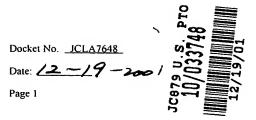


PATENT



ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

ATTENTION: APPLICATION BRANCH

Sir:

Transmitted herewith for filing is the patent application of

Inventors: Yao-Wen Chang, Tao-Cheng Lu

For: MOSFET STRUCTURE WITH REDUCED JUNCTION CAPACITANCE

Enc	losed	are:

- \boxtimes Specification 15 pages.
- 2 Sheets of drawings
- Recordation Form Cover sheet with 2 pages assignment.
- A certified copy of Taiwan Patent Application No. __ dated ___.
- **SIGNED** declaration and power of attorney.
- Return Prepaid postcard.

CLAIMS AS FILED							
FOR	NUMB	ER FII	ED	NUMBE	R EXTRA	RATE	FEE
Basic Fee						\$740.00	\$740.00
Total Claims	19		20 =	= 0	×	\$18.00	\$0.00
Independent Claims	4	_	3 =	: 1	×	\$84.00	\$84.00
If application contains any r	multiple depende	ent clair	n (s), th	en add		\$280.00	\$0.00
	TOTAL	FILING	FEE				\$824.00

A check in the amount of \$824.00 cover the filing fee is enclosed.

A check in the amount of \$40.00 to cover the assignment recording fee.

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Docket No.	: JCLA7648
Applicant(s)	: Yao-Wen Chang, Tao-Cheng Lu
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I hereby certify that the accompanying

Date of Deposit

Transmittal in Duplicate; Specification <u>15</u> pages, <u>2</u> sheets of drawings; **SIGNED** Declaration and Power of Attorney <u>2</u> pages; Recordation Form Cover Sheet and Assignment <u>3</u> pages; Checks for Filing Fee(s); Return Prepaid Postcard

: December 19, 200/

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Michelle Chang